

Syllabus

- **Course Description:**

This course constitutes an introduction to the Computer Engineering area of the Electrical and Computer Engineering Department. Designed around the central theme of a computer, it aspires to provide the necessary foundation for designing a simple processor and understanding how computers compute. Starting with data representation in digital form, it goes on to provide students with the ability to design a circuit for a given algorithmic information processing task. For this purpose, Boolean functions and combinational design are covered, followed by sequential logic design through Finite State Machines. Finally, the two parts are fused through the introduction of basic processor design principles.

- **Course Objectives:** The course aims to:

- ✓ Develop engineering skills in the design and analysis of digital logic components and circuits
- ✓ Make students thoroughly familiar with the basics of gate-level circuit design using simple logic gates then building up to more complex systems
- ✓ Provide hands-on experience and exposure to circuit design using state-of-the-art computer aided design tools and programmable logic devices

- **Target Audience:**

Freshmen considering Computer Engineering as a major and others with a serious interest in technology.

- **Prerequisites:**

None.

- **Instructor:** Diana Cogan

- Email: diana.cogan@utdallas.edu Course Website: <http://elearning.utdallas.edu/>
- Office: ECSN 3.312 Phone: 972-883-6275
- Office hours: Monday, Wednesday & Friday 11:30 a.m. - 12:30 p.m. or by appointment

- **Lectures:**

Section 001: Wednesday 10:00 a.m.- 11:15 a.m. in GR 3.420.

Section 003: Friday 2:30 p.m.- 3:45 p.m. in ECSS 2.415.

Section 004: Monday 2:30 p.m.- 3:45 p.m. in JSOM 1.217.

- **Required Textbook:**

The course does not follow a particular textbook. The lecture slides presented in class will be made available on the course website and should suffice.

- **Additional References:**

- 3 part Verilog video series by Dr. Bill Swartz (on the course website)
- *Principles of Digital Design*, by Daniel D. Gajski, Prentice Hall, 1997.
- *Introduction to Digital Systems*, by Milos D. Ercegovac, Tomas Lang, and Jaime H. Moreno, John Wiley & Sons Inc., 1999.
- *Contemporary Logic Design* (2nd edition), by Randy Katz and Gaetano Boriello, Prentice-Hall, 2003.

- **Important Class Dates:**

- First Class: The week of January 9, 2017
- Monday, January 16, 2017: UTD will be closed for MLK Day - classes will not meet
- *Exam 1*: Sec. 001 - Feb. 22; Sec. 003 - Feb. 24; Sec. 004 - Feb. 27
- Spring Break: March 13-17, 2017
- *Exam 2/Last Class*: Sec. 001 - Apr. 26; Sec. 003 - Apr. 28; Sec. 004 - tbd (see finals schedule)

- **Course Evaluation:**

- Exam 1: 30%
- Exam 2: 30%
- Homework: 10%
- Lab Assignments: 30%

Grading scale:

$90 \leq A- < 93$	$93 \leq A < 97$	$97 \leq A+ \leq 100$
$80 \leq B- < 83$	$83 \leq B < 87$	$87 \leq B+ < 90$
$70 \leq C- < 73$	$73 \leq C < 77$	$77 \leq C+ < 80$
$60 \leq D- < 63$	$63 \leq D < 67$	$67 \leq D+ < 70$
$0 \leq F < 60$		

- **How to Succeed in this Class:**

- **Attend lectures** and take notes.
- Review the lecture slides and your notes frequently.
- Start and complete lab work and homework assignments in a timely fashion.
- See the Teaching Assistants during lab hours.
- See the instructor during office hours or by appointment.
- E-mail the TAs or the instructor. (*PLEASE include EE/CE 1202 in the subject line and email from your UTD address.*)

- **Course Policies:**

- You are responsible for *all* the material covered in class. The course does not follow a textbook, so *don't miss* lectures.
- Two homework problem sets will be assigned during the course of the semester.
- Six laboratory assignments will be given during the course of the semester. Each assignment is one portion of the semester project. In the labs, you will be designing, simulating, and implementing your own Processor on an FPGA board using Xilinx ISE, a Microsoft Windows based software package. The first lab will familiarize you with the tools. The teaching assistants will be available during office hours to answer any questions you may have regarding Xilinx ISE. Details about the lab will be provided as the semester progresses.
- There will be a 10% per day penalty for late labs. Late homework will not be accepted.
- No makeup exams/labs/homework will be offered in this course. Any graded work can be disputed in writing within one week of the return of that work. In such cases, the entire work will be re-graded.
- Copying on examinations, assignments and labs is cheating and is prohibited. Any instances of cheating or plagiarism is academic dishonesty and will be subject to disciplinary penalties according to the UT Dallas policy on scholastic dishonesty. The penalties include the possibility of failure in the course and/or dismissal from the University. Since such dishonesty harms the individual, all students and the integrity of the University, policies on scholastic dishonesty will be strictly enforced. Please carefully read this policy at <http://www.utdallas.edu/deanofstudents/dishonesty/>.
- If a student has to be absent for several classes, e.g. because of job related obligations, he/she will not be eligible for an incomplete grade. In such cases, the student is advised to withdraw from the course.
- Announcements, homework assignments and complementary materials will be posted on the course website. It is the responsibility of each student to check this web page regularly.

- **Syllabus & Tentative Lecture Plan:**

Week/Dates	Assignments		Topics to Be Covered
	New	Due	
1) 1/11,13,9	Lab 0		Lecture 1 - computer basics, binary representation
2) 1/18,20,23			Lecture 2 - base conversion, binary arithmetic
3) 1/25,27,30	Lab 1	Lab 0	Lecture 3 - logic gates, truth tables, Boolean functions
4) 2/1,3,6		Lab 1, pts 1-2	Lecture 4 - Adders, subtractors and multiplexers
5) 2/8,10,13	Lab 2; HW 1	Lab 1, pts 3-4	Lecture 5 - ALU design and board interface
6) 2/15,17,20		Lab 2, pts 1-3; HW 1	Review
7) 2/22/24/27		Lab 2, pts 4-7	EXAM 1 (Lectures 1-5)
8) 3/1,3,6	Lab 3		Lecture 6 - Introduction to sequential circuits
9) 3/8,10,20	Lab 4	Lab 3	Lecture 7 - Introduction to Finite State Machines
10) 3/22,24,27		Lab 4, pt 1	Lecture 7, continued
11) 3/29,31;4/3	Lab 5	Lab 4, pt 2	Lecture 8 - Computer architecture basics
12) 4/5,7,10		Lab 5, pts 1-2	Lecture 9 - Programming the Toy Processor
13) 4/12,14,17	Lab 6; HW 2	Lab 5, pts 3-4	Lecture 10 - State minimization, addressing modes
14) 4/19,21,24		HW 2	Review
15) 4/26,28;tbd*		Lab 6	EXAM 2 (Lectures 6-10)

*Section 004 (Monday class) will take Exam 2 at the time scheduled for the class final.

Descriptions and timelines are subject to change at the discretion of the instructor.

The following information may also be found at: <http://go.utdallas.edu/syllabus-policies>

- **Sharing Confidential Information**

Students considering sharing personal information in email, in person, or within assignments or exams should be aware that faculty members and teaching/research assistants are required by UT Dallas policy to report information about sexual misconduct to the UT Dallas Title IX Coordinator. Per university policy, faculty have been informed that they must identify the student to the UT Dallas Title IX Coordinator. Students who wish to have confidential discussions of incidents related to sexual harassment or sexual misconduct should contact the Student Counseling Center (972-883-2527 or after hours 972-UTD-TALK or 972-883-8255), the Women's Center (972-883-8255), a health care provider in the Student Health Center (972-883-2747), the clergyperson (or other legally recognized religious advisor) of their choice, or an off-campus resource (i.e., rape crisis center, doctor, psychologist). Students who are sexually assaulted, harassed, or victims of sexual misconduct, domestic violence, or stalking, are encouraged to directly report these incidents to the UT Dallas Police Department at 972-883-2222 or to the Title IX Coordinator at 972-883-2218. Additional information and resources may be found at <http://www.utdallas.edu/oiec/title-ix/resources>.

- **Technical Support**

If you experience any issues with your UT Dallas account, contact the UT Dallas Office of Information Technology Help Desk: assist@utdallas.edu or call 972-883-2911.

UT Dallas provides eLearning technical support 24 hours a day/7 days a week. The services include a toll free telephone number for immediate assistance (1-866-588-3192), email request service, and an online chat service. Please use this link to access the UTD eLearning Helpdesk: <http://www.utdallas.edu/elearning/eLearningHelpdesk.html>.

- **Field Trip Policies, Off-Campus Instruction and Course Activities**

Off-campus, out-of-state, foreign instruction/travel, and course-related field trip activities are subject to state law and University policies and procedures regarding travel and risk-related activities.

Detailed information regarding this policy, in accordance to *Texas Education Code*, Section 51.950, can be accessed at the UT Dallas Policy Navigator, <http://policy.utdallas.edu/utdbp3023>, and at <http://www.utdallas.edu/administration/insurance/travel>. Additional information is available from the office of the school dean.

- **Student Conduct and Discipline**

The University of Texas System (Regents' Rule 50101) and The University of Texas at Dallas have rules and regulations for the orderly and efficient conduct of their business. It is the responsibility of each student and each student organization to be knowledgeable about the rules and regulations which govern student conduct and activities. General information on student conduct and discipline is contained in the UT Dallas online catalogs (<http://catalog.utdallas.edu>).

The University of Texas at Dallas administers student discipline within the procedures of recognized and established due process. Procedures are defined and described in the Student Code of Conduct, UTDSP5003 (<http://policy.utdallas.edu/utdsp5003>). Copies of these rules and regulations are available to students in the Office of Community Standards and Conduct, where staff members are available to assist students in interpreting the rules and regulations (SSB 4.400, 972-883-6391) and online at <https://www.utdallas.edu/conduct/>.

A student at the University neither loses their rights nor escapes the responsibilities of citizenship. He or she is expected to obey federal, state, and local laws as well as the Regents' Rules, university regulations, and administrative rules. Students are subject to discipline for violating its standards of conduct whether such conduct takes place on or off campus, or whether civil or criminal penalties are also imposed for such conduct.

- **Academic Integrity**

The faculty expects from its students a high level of responsibility and academic honesty. Because the value of an academic degree depends upon the absolute integrity of the work done by the student for that degree, it is imperative that a student demonstrates a high standard of individual honor in his or her scholastic work.

Academic Dishonesty: Academic dishonesty can occur in relation to any type of work submitted for academic credit or as a requirement for a class. It can include individual work or a group project. Academic dishonesty includes plagiarism, cheating, fabrication, and collaboration/collusion. In order to avoid academic dishonesty, it is important for students to fully understand the expectations of their professors. This is best accomplished through asking clarifying questions if an individual does not completely understand the requirements of an assignment.

Additional information related to academic dishonesty and tips on how to avoid dishonesty may be found here: <https://www.utdallas.edu/conduct/dishonesty/>.

- **Copyright Notice**

It is the policy of the University of Texas at Dallas to adhere to the requirements of the United States Copyright Law of 1976, as amended, (*Title 17, United States Code*), including ensuring that the restrictions that apply to the reproduction of software are adhered to and that the bounds of copying permissible under the fair use doctrine are not exceeded. Copying, displaying, reproducing, or distributing copyrighted material may infringe upon the copyright owner's rights. Unauthorized distribution of copyrighted material, including unauthorized peer-to-peer file sharing, may subject students to appropriate disciplinary action as well as civil and criminal penalties. Usage of such material is only appropriate when that usage constitutes "fair use" under the Copyright Act. For more information about the fair use exemption, see <http://copyright.lib.utexas.edu/copypol2.html>. As a UT Dallas student, you are required to follow UT Dallas' copyright policy (UTDPP1043 at <http://policy.utdallas.edu/utdpp1043>) and the UT System's policy, UTS107 at <http://www.utsystem.edu/board-of-regents/policy-library/policies/uts107-use-copyrighted-materials>.

- **Email Use**

The University of Texas at Dallas recognizes the value and efficiency of communication between faculty/staff and students through electronic mail. At the same time, email raises some issues concerning security and the identity of each individual in an email exchange. All official student email correspondence will be sent only to a student's UT Dallas email address and UT Dallas will only consider email requests originating from an official UT Dallas student email account. This allows the University to maintain a high degree of confidence in the identity of each individual's corresponding via email and the security of the transmitted information. The University of Texas at Dallas furnishes each student with a free email account that is to be used in all communication with university personnel. The Office of Information Technology provides a method for students to have their UT Dallas mail forwarded to other email accounts. To activate a student UT Dallas computer account and forward email to another account, go to <http://netid.utdallas.edu>.

- **Class Attendance**

Regular and punctual class attendance is expected. Students who fail to attend class regularly are inviting scholastic difficulty. Absences may lower a student's grade where class attendance and class participation are deemed essential by the instructor. In some courses, instructors may have special attendance requirements; these should be made known to students during the first week of classes.

- **Withdrawal from Class**

The administration at UT Dallas has established deadlines for withdrawal from any course. These dates and times are published in the Comet Calendar (<http://www.utdallas.edu/calendar>) and in the Academic Calendar (<http://www.utdallas.edu/academiccalendar>). It is the student's responsibility to handle withdrawal requirements from any class. In other words, a professor or other instructor cannot drop or withdraw any student unless there is an administrative drop such as the following:

- Have not met the prerequisites for a specific course
- Have not satisfied the academic probationary requirements resulting in suspension
- Office of Community Standards and Conduct request
- Have not made appropriate tuition and fee payments
- Enrollment is in violation of academic policy
- Was not admitted for the term in which they registered

It is the student's responsibility to complete and submit the appropriate forms to the Registrar's Office and ensure that he or she will not receive a final grade of "F" in a course if he or she chooses not to attend the class after being enrolled.

- **Student Grievance Procedures**

Procedures for student grievances are found in university policy UTDSP5005 (<http://policy.utdallas.edu/utdsp5005>). In attempting to resolve any student grievance regarding disputes over grades, application of degree plan, graduation/degree program requirements, and thesis/and dissertation committee, adviser actions and/or decisions, evaluations, and/or other fulfillments of academic responsibility, it is the obligation of the student first to make a serious effort to resolve the matter with the instructor, supervisor, administrator, or committee with whom the grievance originated.

- **Incomplete Grade Policy**

As per university policy, incomplete grades may be given, at the discretion of the instructor of record for a course, when a student has completed at least 70% of the required course material but cannot complete all requirements by the end of the semester. An incomplete course grade (grade of 'I') must be completed within the time period specified by the instructor, not to exceed eight (8) weeks from the first day of the subsequent long semester. Upon completion of the required work, the symbol 'I' may be converted into a letter grade (A through F). If the grade of Incomplete is not removed by the end of the specified period, it will automatically be changed to F.

- **AccessAbility Services**

It is the policy and practice of The University of Texas at Dallas to make reasonable accommodations for students with properly documented disabilities. However, written notification from the Office of Student AccessAbility (OSA) is required. If you are eligible to receive an accommodation and would like to request it for this course, please discuss it with your professor and allow one week advance notice. Students who have questions about receiving accommodations, or those who have, or think they may have, a disability (mobility, sensory, health, psychological, learning, etc.) are invited to contact OSA for a confidential discussion. OSA is located in the Student Services Building, SSB 3.200. They can be reached by phone at 972-883-2098, or by email at studentaccess@utdallas.edu.

- **Religious Holy Days**

The University of Texas at Dallas will excuse a student from class or other required activities, including examinations and travel time for the observance of a religious holy day for a religion whose places of worship are exempt from property tax under Section 11.20, of the *Texas Tax Code*.

Students are encouraged to notify the instructor or activity sponsor as soon as possible regarding the absence, preferably in advance of the assignment.

Excused students will be allowed to take missed exams or complete assignments within a reasonable time after the absence: a period equal to the length of the absence, up to a maximum of one week. A student who notifies the instructor and completes any missed exam or assignment may not be penalized for the absence. A student who fails to complete the exam or assignment within the prescribed period may receive a failing grade for that exam or assignment.

If a student or an instructor disagrees about the nature of the absence [i.e., for the purpose of observing a religious holy day] or if there is similar disagreement about whether the student has been given a reasonable time to complete any missed assignments or examinations, either the student or the instructor may request a ruling from the President of UT Dallas or from the President's designee. The chief executive officer or designee must take into account the legislative intent of *Texas Education Code* 51.911(b), and the student and instructor will abide by the decision of the chief executive officer or designee.

- **Resources to Help You Succeed**

The Office of Student Success operates the Student Success Center (SSC, <http://www.utdallas.edu/studentssuccess>), which offers assistance to students in the areas of writing, mathematics, communication, multiple science fields, reading, study skills, and other academic disciplines. These services are available through individual and small group appointments, workshops, short courses, and a variety of online and instructional technologies. All students enrolled at UT Dallas are eligible for these services.

The **Math Lab** gives short-term and semester long support for a variety of introductory and advanced mathematics courses. Students may drop in to visit with a math tutor on a regular basis. Comet card is required.

The **Writing Center** offers a collaborative learning environment for one-to-one and small group assistance with general and advanced writing assignments and overall writing skills. Scheduling an appointment is strongly recommended, but walk in appointments are possible if a tutor is available.

The **Peer Tutoring** program offers free tutoring assistance in multiple locations for many of the historically challenging undergraduate subjects at UT Dallas. Tutoring sessions, offered every weekday on a drop-in basis, are one-on-one or in a small group format. The sessions are designed to meet students' individual questions and needs related to course/subject concepts. All peer tutors are current UT Dallas students who made an A- or better in the course and have a strong faculty/staff recommendation. Students should check the Student Success Center website each semester for subject offerings and session times.

The **Peer-Led Team Learning (PLTL)** program provides an active, engaged learning experience for students who meet in small groups once a week with a Peer Leader who helps guide them through a potentially difficult gateway course. Students that attend sessions regularly typically earn a half to a whole letter grade higher than students that do not participate in the PLTL program.

Supplemental Instruction (SI) provides free, peer-facilitated weekly study sessions for students taking historically difficult courses. SI sessions encourage active, collaborative learning based on critical thinking and transferable study skills. SI leaders attend lectures, take notes, and read assigned material just like the enrolled students. Students should check the SSC website for subject and session times.

The **Communication Lab (CommLab)** offers one-on-one and group consultations where you will gain practical feedback for improving oral and group presentations.

Success Coaches are available for individual student appointments to discuss study skills, time management, note taking, test taking and preparation, and other success strategies.

The Student Success Center's main office is located in the McDermott Library Building and can be contacted by calling 972-883-6707 or by sending an email to ssc@utdallas.edu.

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