



Course Syllabus

Course Information

CS/CE 4341.0U1 Digital Logic & Computer Design - Summer 2025

Lecture Info: TuTh: 3:00 pm - 5:15 pm ; Class Room location: SCI 3.250

Professor's Contact Information

Professor: Dr. Gity Karami

Office Phone: 972-883-4204

Office Location: ECSS 3.202

Email: gity.karami@utdallas.edu

Office hours: TuTh: 11:30 am – 12:15 pm by appointment

Signup link: <https://calendly.com/gxk180009/virtual-office-hours>

Please sign up in advance (at least a few hours before your scheduled meeting)

Course Modality and Expectations

- **Instructional Mode:** Traditional Classroom/Laboratory
- **Course Platform:** All instruction will be through the eLearning platform, where all the course materials including links to the recorded/online resources will be posted. We will also use Microsoft Team for Virtual office hours and piazza as asynchronous Q&A platform.
- **Expectations:** Regular and punctual class attendance is expected. Students who fail to attend class regularly are inviting scholastic difficulty. Active participation on piazza is also expected.

Class Recordings:

The class's lectures may be recorded and the links posted in eLearning. Additionally, the instructor may record other meetings of this course, and such recordings will be posted in eLearning too. **Students are expected to follow appropriate University policies and maintain the security of passwords used to access recorded material.** Unless the Office of Student AccessAbility has approved the student to record the instruction, students are expressly prohibited from recording any part of this course. **Recordings may not be published, reproduced, or shared with those not in the class, or uploaded to other online environments** except to implement an approved Office of Student AccessAbility accommodation. Failure to comply with these University requirements is a violation of the Student Code of Conduct.

Piazza:

We'll be using piazza as asynchronous Q&A platform. The quicker you begin asking questions on Piazza (rather than via emails), the quicker you'll benefit from the collective knowledge of your classmates and instructor. I encourage you to

ask questions when you're struggling to understand a concept. The link to enroll in Piazza is available in e-learning.

Microsoft Team:

We'll be using Microsoft Team for Virtual office hours. Virtual office hours will be held using Microsoft Team by appointment. Live lectures may also be available on Microsoft Team during the class time.

Class Participation:

Regular and punctual class attendance is expected. Students who fail to follow the class materials regularly are inviting scholastic difficulty. The course's material gets much more complex as the course progresses, and it is typically very difficult to catch up with missed classes. Active participation on piazza is also expected.

Class Materials:

The instructor may provide class materials that will be made available to all students registered for this class. These materials may be downloaded during the course, however, these materials are for registered students' use only. Classroom materials may not be reproduced or shared with those not in the class or uploaded to other online environments, except to implement an approved Office of Student AccessAbility accommodation. Failure to comply with these University requirements is a violation of the Student Code of Conduct.

Course Prerequisites:

(Prerequisites: (CE 2310 or EE 2310) or (CS 3340 or SE 3340 or TE 3340) and PHYS 2326. Corequisite: (CS 4141 or TE 4141). Credit cannot be received for both courses, (CS 4341 or TE 4341) and (CE 3320 or EE 3320).

Course Description:

CS/CE 4341- **Digital Logic and Computer Design** (3 semester credit hours); Boolean algebra and logic circuits; synchronous sequential circuits; gate level design of ALU, registers, and memory unit; register transfer operations; design of data path and control unit for a small computer; Input-Output interface. Credit cannot be received for both courses, **CS 4341** and (**CE 3320** or **EE 3320**).

Text Book:

David M. Harris and Sarah L. Harris, "Digital Design and Computer Architecture", 2nd ed., Morgan Kaufmann, ISBN: 978-0123944245.

M. Morris Mano, Charles Kime, "Logic and Computer Design Fundamentals, 4th ed", Prentice Hall, 2007, ISBN: 978-0131989269.

*supplementary materials will be posted in the e-learning

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Learning Objectives:

- Ability to analyze, minimize, and design gate-level combinational logic circuits using Boolean algebra and 3 and 4 variable Karnaugh Maps.
- Ability to analyze and design simple synchronous sequential circuits
- Ability to analyze, design and utilize digital logic components such as adders, multiplexers, decoders, registers, and counters.
- Ability to understand RAM and ROM memory components, and utilize these in digital logic design.
- Ability to design computer components such as Arithmetic-Logic-Unit (ALU) and data path.
- Ability to understand the basics of hardware description languages such as Verilog or VHDL.

Tentative Course Schedule:

Week	Date	Material Covered
1	June 3- June 5	Syllabus - Chapter 1
2	June 10- June 12	Chapter 2
3	June 17- June 19	Chapter 2- No class
4	June 24- June 26	Chapter 2- Chapter 3
5	July 1- July 3	Chapter 3 - Chapter 5
6	July 8- July 10	Chapter 5 - Midterm Exam
7	July 15- July 17	Chapter 4 - Chapter 6
8	July 22- July 24	Chapter 6
9	July 29- July 31	Chapter 7
10	August 5- August 7	Chapter 8
11	August 12	Final Exam

Course Works and Grading Policies:

Exams: 60% (Midterm Exam: 20% , Final Exam: 35%, Quizzes 5%)

Assignments: 40% (Four Assignments, each 10%)

*Instructor reserves the right to alter these weights or make changes as she sees fit.

Grades will be assigned according to the following scale

A+	97 and above
A	94 - 96 (94 or more and less than 97)
A-	90 - 93 (90 or more and less than 94)
B+	87 - 89 (87 or more and less than 90)
B	84 - 86 (84 or more and less than 87)
B-	80 - 83 (80 or more and less than 84)
C+	77 - 79 (77 or more and less than 80)
C	74 - 76 (74 or more and less than 77)
C-	70 - 73 (70 or more and less than 74)
D	60 - 69 (60 or more and less than 70)
F	Below 60

*We will have reading assignment every week.

Exam: There will be only two exams in this course. The final exam is a comprehensive exam. During the exams, you are allowed to access the course home page in e-learning. The lecture slides, lecture notes and quiz solutions will be visible during the exams. You are responsible for being available during the exam times. If you cannot make an exam time due to a valid excuse, you must let me know BEFORE the exam date and time. Medical emergencies will require a note from your Doctor. Missed exam will result in a grade of 0 for that exam.

Assignments: Doing assignments is vital for meeting the learning objectives and succeeding in this course. There will be four assignments in this course. You must work on the assignments with a partner and submit them on e-learning. No e-mail submissions are accepted. No late submissions are accepted. So, please plan accordingly, do not leave your submissions to the last minute. Everybody submits his/her work very easily via e-Learning, you can do it, too. If you encounter a problem during e-Learning submission, please contact 24/7 e-Learning Help IMMEDIATELY. This help is available 24/7 at:

e-Learning Help URL: <http://www.utdallas.edu/elearning/eLearningHelpdesk.html>
e-Learning Help Phone: 1 866 588 3192

If you believe that you have a valid excuse for your work being late, then you must make arrangements with the instructor BEFORE the due date. Late sub-

missions are not permitted once the graded assignment has been returned to students. Medical excuses will require a note from your Doctor.

Quizzes: You are supposed to work on each quiz individually or with a partner. You are allowed to use the text book and lecture slides during the quizzes. You must submit them on e-learning. No e-mail submissions are accepted.

Grading Disputes: All grade disputes must be reported in person to the instructor or TA using grading dispute form within 3 days of the grade being posted in eLearning. Uncontested grades will become final after 3 days and cannot be disputed later.

Academic Dishonesty: You should do your own work on exam and assignments. Copying another student's work is not acceptable. Any indication of cheating and/or plagiarism on an exam/assignment will be an automatic 0 (zero) for the exam/assignment for all students involved. Solutions copied from the internet, instructor's manual, etc. will be also given zero credit. Please note that suspected incidents will be reported to the Office of Community Standards and Conduct.

Communications: I will be communicating with you via eLearning, piazza, and e-mail. If you need to send me an e-mail make sure it is using your UTD e-mail address. Please choose appropriate subjects for your emails. Always include your course and section number in the subject of your emails (for example, CS 4341.0U1- Exam). I won't answer your emails, if you don't put the course number and section number in the subject of your emails. Make sure you are checking eLearning announcements and checking your UTD e-mail frequently. I can't respond to you via gmail or any other non-UTD e-mail system. I need to verify that you are my student and I can only do that with the UTD e-mail system.

Comet Creed: "As a Comet, I pledge honesty, integrity, and service in all that I do."

Additional Policies: Please visit <http://go.utdallas.edu/syllabus-policies> for all other University policies

Descriptions and timelines are subject to change at the discretion of the Professor.